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APPLICATION NO.	FILING DATE		FIRST NAMED INVE	NTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/825,946	04/05/2001	Junji No		· · · · · · · · · · · · · · · · · · ·	501.39932X00	5570	
TITLE OF INVENTION: SI DEVICE	EMICONDUCTOR INTEG	RATED CIRCUIT	Γ DEVICE AND FA	ABRICATION METHO	DD FOR SEMICONDUCTOR	R INTEGRATED CIRCUIT	
APPLN. TYPE	SMALL ENTITY	ISSUE F		PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	nonprovisional NO)	\$300	\$1630	06/02/2004	
EXAMINER		ART UN	IIT (CLASS-SUBCLASS]		
VU, HUNG K		2811		257-687000	_		
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Renesas Technology Corp. Tokyo, Japan							
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Ralph T. Webb	91 3/3 JOA7U	June	2, 2004	1			
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